

Performance Optimisation and Productivity

A Centre of Excellence in Computing Applications

POP Newsletter 2 -- Issue September 2016

A big welcome to the second newsletter from the <u>POP (Performance Optimisation and</u> <u>Productivity) CoE (Centre of Excellence)</u>. We're an EU funded project and offer free support to organisations within the EU to help improve performance of parallel software.

This issue includes:

- How to apply for help from our code optimisation experts
- The new POP Helpdesk
- The POP CoE LinkedIn group
- The new webpage for POP training and learning material
- POP partner profiles
 - o The Barcelona Supercomputer Centre
 - o The Jülich Supercomputing Centre
- A few highlights from recent POP work

For those who missed it our first newsletter is now available <u>via our website</u>, articles included an introduction to the POP Vision, our services, the tools we use, and more.

Apply for free help with code optimisation

We offer a range of free services designed to help organisations within the EU to improve performance of parallel software. If you're not getting the performance you need from parallel software, please apply for our help via the short <u>Service Request Form</u> or <u>email us</u> to discuss further.

These services are funded by the European Union Horizon 2020 research and innovation programme - there's no direct cost to our users!

POP LinkedIn Group

We now have a <u>POP LinkedIn Group</u>, please join us there to keep up to date with our news, ask us questions, and take part in discussions about the POP services, performance analysis methods and tools.

The new POP Helpdesk

Long term support is something we take seriously, and for current and past POP users our performance experts are now available via our <u>email helpdesk</u> to answer your questions and point you to the appropriate resources.

If you're having trouble analysing the code changes you've made, unsure of your next steps and want to check before making changes, or want to ask a question about parallel performance optimisation, just email our help desk.

New blog post - nearly 50% higher performance!

This blog post describes our proof of concept work to improve performance of HPC fluid dynamics software written in Fortran using MPI & OpenMP. Read the blog post <u>here</u>.

This is our third piece of work on this code. Our initial involvement was an Audit followed by a Performance Plan.

The Audit comprehensively analysed code performance and identified problems with load imbalance and made recommendations on improving serial performance. The Performance Plan took a more in depth look at the cause of the load imbalance and made further recommendations.

Audits, Performance Plans and Proof of Concept work are free of charge for organisations within the EU, these services are described in more detail on the <u>POP website</u>.

POP Training & Learning

We recently added a new section to the POP website to make our <u>learning material</u> available, so far this includes tutorials on the tools we use in performance analysis. These tools are open source and <u>available to all</u>.

Anyone interested in characterising performance of parallel software is recommended to read the <u>presentation on efficiency metrics</u> describing how we characterise performance, e.g. how we measure parallel efficiency, load balance, communication efficiency and computational efficiency.

In addition to online learning material POP also offer performance analysis workshops to developers of parallel applications. These workshops aim to work with developers on their actual code (rather than prepared exercises) to obtain performance analyses on real applications. The workshops are complemented by lectures on the performance analysis tools.

The next workshop will take place in Ostrava in the Czech Republic mid of December 2016.

For more information on POP training please email us.

POP Partner Profiles

The POP CoE consists of <u>6 partners</u> and in the <u>first newsletter</u> we introduced two of these: The Numerical Algorithms Group and the HPC Group at RWTH Aachen.

In this issue we take a closer look at the <u>Barcelona Supercomputer Center</u> (BSC) and the <u>Jülich Supercomputing Centre</u> (JSC).

The High Performance Computing Center Stuttgart and TERATEC will be introduced in subsequent newsletters.

The Barcelona Supercomputer Center Performance Tools Group



The development of performance analysis tools in Barcelona started in 1991 within a research group at the Technical University of Catalonia (UPC). After BSC's formation the Performance Tools Group became one of the main research areas within UPC's Computer Sciences Department.

BSC are internationally recognized as HPC specialists and expert tool developers. From the very beginning BSC promoted designs and methodologies with flexibility, simplicity and the ability to interact with qualitative and quantitative information at their heart. These features allow performance analysis experts to use the same tools whether using novel homogeneous and heterogeneous multi-core architectures or using more traditional highly scalable cluster systems.

From BSC's perspective parallel performance analysis can be considered in some sense an art, where the experience and intuition of the analyst drives the analysis and determines the quality of the results. In addition, the group's philosophy is that more people would be undertaking performance analysis of their software if they had the necessary skills and expertise. For these reasons the Performance Tools Group at BSC continuously provides training both locally and around the world.

It was BSC's tools, expertise, and the knowledge acquired over the last 25 years on performance analysis for HPC systems that motivated BSC to initiate and coordinate the POP service-oriented project.

The Jülich Supercomputing Centre

Operating since 1987 the <u>Jülich Supercomputing Centre (JSC)</u> at Forschungszentrum Jülich was the first German supercomputing centre, and with the Jülich Institute for Advanced Simulation is continuing a long tradition of scientific computing at Jülich. HPC resources of the highest performance levels are made available to researchers in Germany and Europe by means of an independent peer-review process, and currently JSC operates one of the most powerful supercomputers in Europe, JUQUEEN.

About 200 experts and contacts covering all aspects of supercomputing and simulation sciences work at JSC. JSC's research and development concentrates on mathematical and numerical modelling, especially parallel algorithms for quantum chemistry, molecular dynamics and Monte-Carlo simulations. The focus in computer sciences is on cluster computing, performance analysis of parallel programs, visualization, computational steering and federated data services.

Services for POP are provided by the <u>SoftWare Analysis and Tools</u> (SWAT) Team of the JSC Application Support division. The mission of this division is support of HPC users and efficient usage of their applications on HPC systems at Forschungszentrum Jülich. Traditionally our users are based at German universities, national research institutes, and the Research Centre itself; but



increasingly they come from European institutes outside Germany, from industry, and from European infrastructure projects like PRACE, LinkSCEEM, ESMI, and SOPHIA, in which our institute is actively involved.

Responsibilities of the SWAT Team include:

- optimizing user application codes
- training HPC users in performance analysis as part of PRACE PATC and <u>VI-HPS</u> training activities, as well as summer and winter schools
- designing, implementing and maintaining performance tools based around the <u>Scalasca</u> toolset
- participating in national and international research projects and the Jülich Exascale efforts IESP/BDEC, EESI, EIC, and ECL

Recent analysis highlights

We've recently completed various Audits, Performance Plans and Proofs of Concept, here are a few examples of things we found.

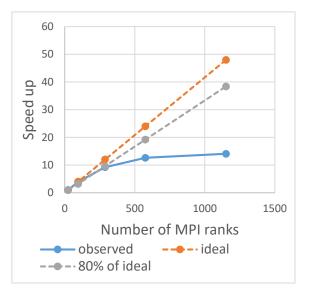
Poor instruction scalability

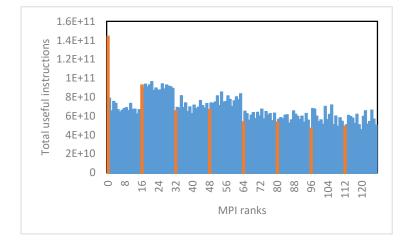
A Fortran code using MPI was known to scale poorly on a large number of cores. Using Extrae we generated performance data for a

Number of cores	Instructions scalability
96	100%
288	85%
6912	76%
1152	54%

full size computation on 2300+ cores. Our analysis of this data identified the largest cause of poor parallel performance was that for constant

problem size the total number of instructions increase with the number of cores. The additional computational work with increased core count was unknown to the code developers.





Inter- node load imbalance

POP were asked to audit a code with known poor parallel scaling. Using Extrae and Paraver we identified significant imbalance in the number of instructions executed on each compute node, resulting in large wait times at the end of MPI regions.

As a follow up we've offered to undertake a Performance Plan to investigate options for dynamic load balancing.

Load balance analysis

Score-P & PAPI were used to undertake a performance audit of Fortran90 MPI software, and the data obtained used to calculate our usual performance metrics including parallel efficiency, load balance, communication efficiency, plus computational, instructions and IPC scalability. Significant load imbalance was identified and the functions responsible were investigated further, two were found to have particularly poor load balance with respect to the number of useful instructions executed. A low value of useful IPC was also found which should be investigated further.

Analysing impact of multicore sharing

Recent analysis of hybrid MPI + OpenMP Fortran code investigated the effect of increasing threads per node for constant number of MPI processes per node. An analysis using the Paraver tool suite demonstrated a drop in global efficiency as thread count increased. It was predominantly caused by reduced IPC (instructions per cycle) due to resource sharing on the node.

Further analysis using the Cluster Analysis Toolkit identified exactly which of the computation bursts was affected the most from poor IPC due to resource sharing. This demonstrates nicely the power of these tools in identifying the exact causes of decreased parallel efficiency.

