

TOOLS FOR GPU COMPUTING

With focus on NVIDIA GPUs

03.12.2019 | MICHAEL KNOBLOCH



MOTIVATION

- IDEs
- Porting
- Libraries

Debugger:

- CUDA-MEMCHECK
- CUDA-GDB
- TotalView
- DDT

Make it work, make it right, make it fast.

Kent Beck

Performance Tools:

- NVIDIA Visual Profiler
- NVIDIA Nsight System
- NVIDIA Nsight Compute
- Score-P
- Vampir
- Performance Reports
- TAU
- HPCToolkit





GPU PROGRAMMING MODELS CURRENT STATE OF THE MESS



TRADITIONAL HPC

- Inter-node:
 - MPI

- Intra-node:
 - OpenMP
 - Pthreads

• C/C++ and Fortran



MODERN HPC

Inter-node:

- MPI
- PGAS (SHMEM, GASPI, ...)

Intra-node:

- OpenMP
- Pthreads
- Tasking, C++11 threads, TBB, ...
- C/C++, Fortran and Python



GPU PROGRAMMING

Low-level:

- CUDA (NVIDIA), ROCm (AMD)
- OpenCL

Pragma-based:

- OpenACC
- OpenMP target
- On top: SYCL, oneAPI, HIP, KOKKOS, ...



```
uliiloz i maximuex - ilistetementinuex;
▼ S findmax
                                      uint32 t nextElement;
                                                                                                                                        inttypes.h
                                      uint32 t i = firstElementIndex + threadsCount;
 ▼ Binaries
                                                                                                                                        stdio.h
  ▶ 🌣 findmax - [x86_64, sm_10
                                                                                                                                        stdlib.h
                                      for (; i < ARRAY SIZE; i += threadsCount) {</pre>
                                          nextElement = array[i];
 ▶ 🛍 Includes
                                                                                                                                        string.h
                                          if (nextElement > max) {
 ▼ 🕮 STC
                                                                                                                                        # ARRAY SIZE
                                              max = nextElement:
  ▶ c findmax.cu
                                                                                                                                        # BLOCKS
                                              maxIndex = i:
                                          }
 Debug
                                                                                                                                        # THREADS
                                                                                                                                        # MEMORY BANKS
                                      threadMax[threadIdx.x] = max;
                                                                                                                                        # CUDA CHECK RETURN()
                                      threadMaxIdx[threadIdx.x] = maxIndex;

    findMaxSingleThread(uint32

                                      reduce(threadMax, threadMaxIdx);
                                                                                                                                        reduce(uint32_t*, uint32_t*):
                                      if (!threadIdx.x) { // After reduce max will be in thread 0

    cudaFindGlobalMax(uint32_t

                                          array[blockIdx.x] = threadMax[0];
                                                                                                                                        cudaFindMax(uint32_t*): voice
                                          array[blockIdx.x + BLOCKS] = threadMaxIdx[0];
                                                                                                                                        hostFindMax(const uint32_t[]

    deviceFindMax(const uint32_

                                                                                                                                        initArray(uint32 t* const): vo
                                @uint32 t hostFindMax(const uint32 t array[], uint32 t *index, const uint32 t arrayLength) {
                                                                                                                                        verifyResult(uint32_t, uint32_
                                      uint32 t i. max = 0:
                                      for (i = 0; i < arrayLength; i++) {
                                                                                                                                        main(int, char**): int
                                          if (array[i] > max) {
                                              *index = i;
                                              max = array[i];
```

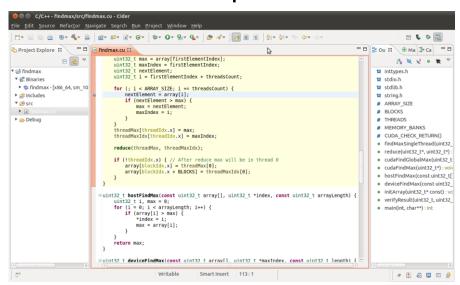
MAKE IT WORK DEVELOPMENT OF GPU APPS



IDE

Integrated Development Environment

- Integrates Editor, Build system, Debugger, and Profiler
- NVIDIA Nsight (Linux: Eclipse, Windows: Visual Studio)
- Nsight Code Editor
 - CUDA aware code completion and inline help
 - CUDA code highlighting
 - CUDA aware refactoring



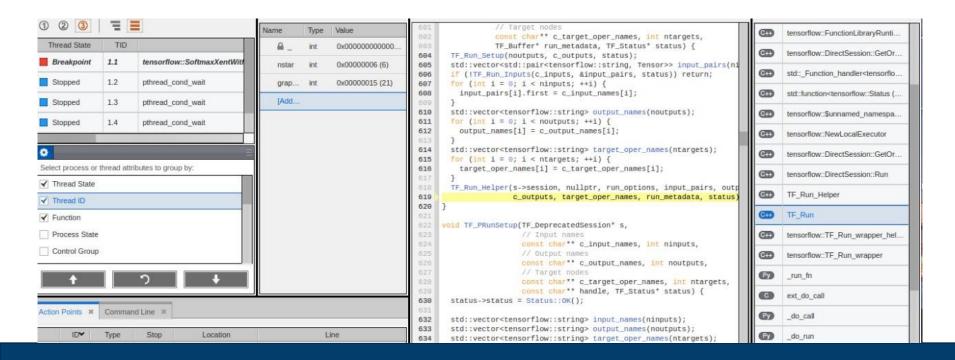


PORTING

- Several tools exist helping expose parallelism
- Example: Appentra Parallelware Trainer
 - Identifies parallelizable sections in sequential applications
 - Supports OpenMP and OpenACC
 - Supports versioning of changes
 - Start program directly from GUI

```
Project Explorer
                                          Code Editor
                                                                                                                           Version Manager
                            // Compute sparse matrix-vector multiplication
// condition atmux(double *val, double *x, double *y, int *col_ind,
                                                                                                                            void atmux(double *val, double *x, double *y, int *col
atmux.c
                                                                                                                                  for (int t = 0; t < n; t++)
                                    for (int t = 0; t < n; t++)
Makefile
■ README md
                                                                                                                                 for (int i = 0; i < n; i++) {
    for (int k = row_ptr[i]; k < row_ptr[i + 1]; k+</pre>
                                       #pragma omp parallel default(none) shared(col_ind, n, row_p
                                                                                                                                           y[col_ind[k]] = y[col_ind[k]] + x[i] * val[
                                      for (int i = 0; i < n; i++) {
   for (int k = row_ptr[i]; k < row_ptr[i + 1]; k++) {</pre>
                                                 y[col_ind[k]] = y[col_ind[k]] + x[i] * val[k];
                                                                                                                     26 int main(int argc, char *argv[]) {
                                                                                                                                 int param_iters = 10;
                                 int main(int argc, char *argv[]) {
                                                                                                                                      printf("Usage: %s <n>\n", argv[0]):
                                       double param_sparsity = 0.66;
                                       if (argc != 2) {
                                                                                                                                  // Reads the test parameters from the command line
                                            printf("Usage: %s <n>\n", argv[0]);
                            /home/daniel.otero/devel/samples/ATMUX/atmux.c line 19: Parallel sparse reduction pattern identified for variable 'y' with associative, commutative
                            /home/daniel.otero/devel/samples/ATMUX/atmux.c line 19: Available parallelization strategies for <u>variable</u> 'y
                            /home/daniel.otero/devel/samples/ATMX//atmx.c line 19: #1 OpenMP <u>stomic</u> access (* implemented)
/home/daniel.otero/devel/samples/ATMX/atmx.c line 19: #2 OpenMP <u>exploit privatization</u>
/home/daniel.otero/devel/samples/ATMX/atmx.c line 19: Loop parallelized with multithreading using OpenMP <u>directive</u> 'for
                            /home/daniel.otero/devel/samples/ATMUX/atmux.c line 19: Parallel region defined by OpenMP directive 'parallel
                                                                       Output Consoles
```





MAKE IT RIGHT DEBUGGER AND MEMORY ANALYZER



DEBUGGER COMPATIBILITY MATRIX

Tool	CUDA	OpenACC	OMPD	OpenCL
CUDA- MEMCHECK	✓	*	*	×
CUDA-GDB	\checkmark	*	×	×
TotalView	✓	✓	**	×
DDT	\checkmark	\checkmark	×	×

= Indirect support via CUDA (Nvidia only)

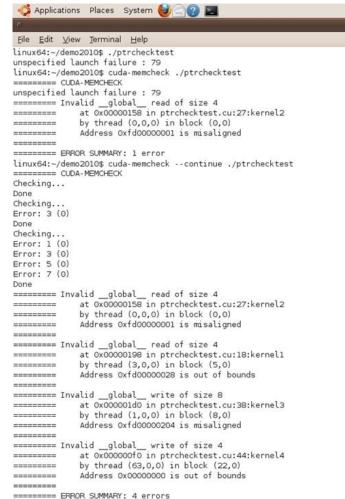
** = Prototype with non-public OMP(D) runtime



CUDA-MEMCHECK



- Valgrind for GPUs
- Monitors hundreds of thousands of threads running concurrently on each GPU
- Reports detailed information about global, local, and shared memory access errors (e.g. out-of-bounds, misaligned memory accesses)
- Reports runtime executions errors (e.g. stack overflows, illegal instructions)
- Reports detailed information about potential race conditions
- Displays stack back-traces on host and device for errors
- And much more
- Included in the CUDA Toolkit

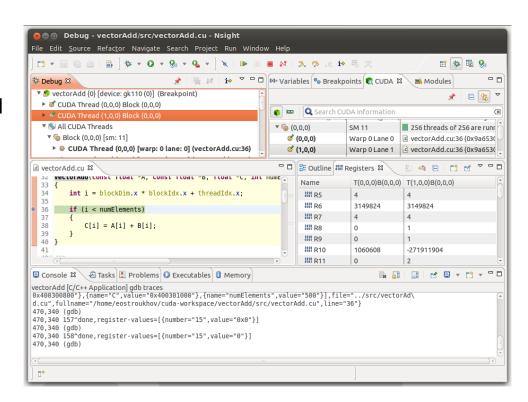




CUDA-GDB



- Extension to gdb
- CLI and GUI (Nsight)
- Simultaneously debug on the CPU and multiple GPUs
- Use conditional breakpoints or break automatically on every kernel launch
- Can examine variables, read/write memory and registers and inspect the GPU state when the application is suspended
- Identify memory access violations
 - Run CUDA-MEMCHECK in integrated mode to detect precise exceptions.

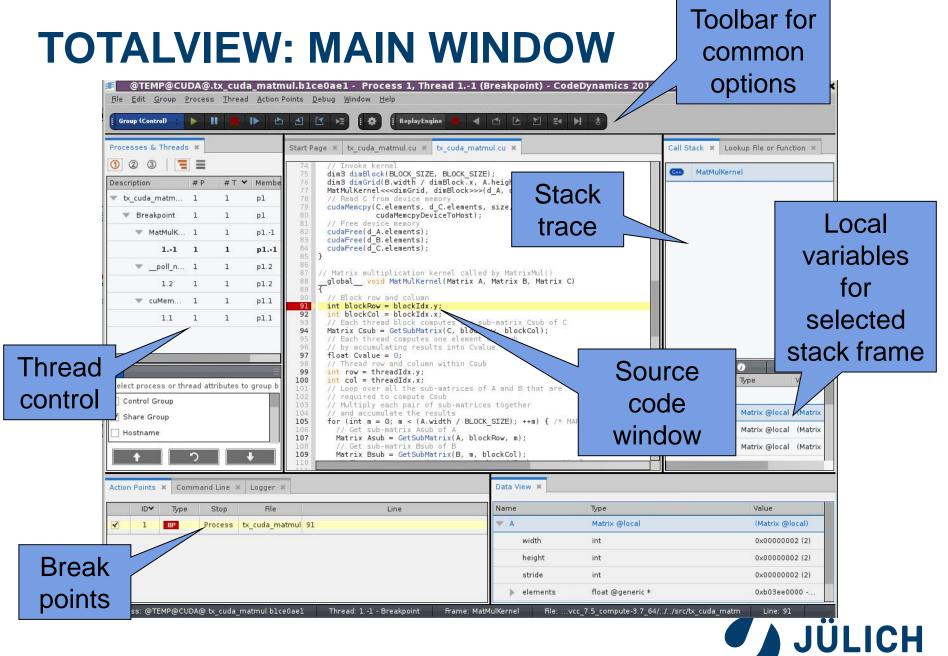






- UNIX Symbolic Debugger for C/C++, Fortran, Python, PGI HPF, assembler programs
- JSC's "standard" debugger
- Special, non-traditional features
 - Multi-process and multi-threaded
 - Multi-dimensional array data visualization
 - Support for parallel debugging (MPI: automatic attach, message queues, OpenMP, Pthreads)
 - Scripting and batch debugging
 - Advanced memory debugging
 - CUDA and OpenACC support
- http://www.roguewave.com



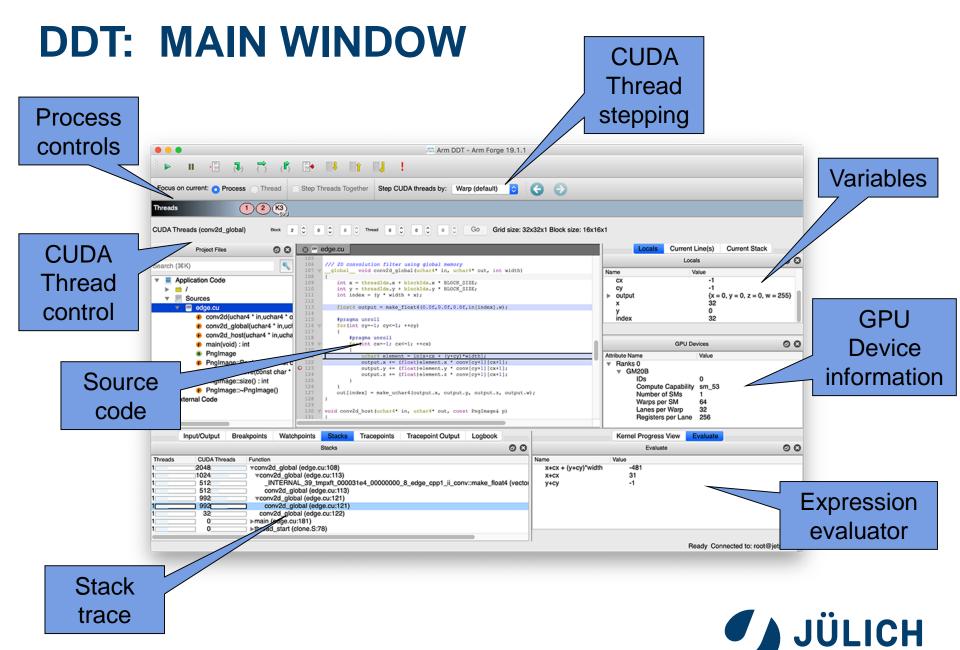


Forschungszentrum

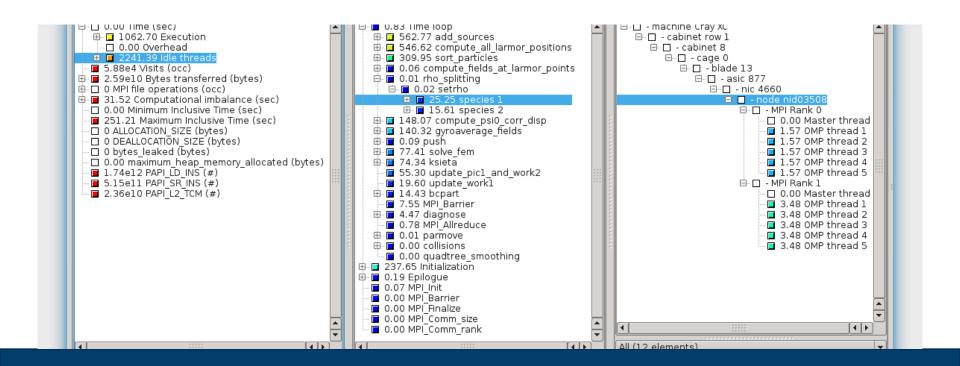


- UNIX Graphical Debugger for C/C++, Fortran, and Python programs
- Modern, easy-to-use debugger
- Special, non-traditional features
 - Multi-process and multi-threaded
 - Multi-dimesional array data visualization
 - Support for MPI parallel debugging (automatic attach, message queues)
 - Support for OpenMP (Version 2.x and later)
 - Support for CUDA and OpenACC
 - Job submission from within debugger
- https://developer.arm.com





Forschungszentrum



MAKE IT FAST PERFORMANCE ANALYSIS TOOLS



PERF TOOL COMPATIBILITY MATRIX

Tool	CUDA	OpenACC	OMPT	OpenCL
Score-P	✓	✓	**	✓
NVIDIA Tools	\checkmark	\checkmark	×	×
Perf. Reports	\checkmark	*	×	×
TAU	\checkmark	\checkmark	**	\checkmark
HPCToolkit	✓	×	**	×
Extrae	√	×	×	√

* = Indirect support via CUDA (Nvidia only)

** = Prototype with non-public OMP(T) runtime



ARM PERFORMANCE REPORTS



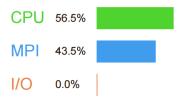
- Single page report provides quick overview of performance issues
- Works on unmodified, optimized executables
- Shows CPU, GPU, memory, network and I/O utilization
- Supports MPI, multi-threading and accelerators
- Saves data in HTML, CVS or text form
- https://www.arm.com/products/development-tools/server-andhpc/performance-reports



EXAMPLE PERFORMANCE REPORTS

Summary: cp2k.popt is CPU-bound in this configuration

The total wallclock time was spent as follows:



Time spent running application code. High values are usually good.

This is average; check the CPU performance section for optimization advice.

Time spent in MPI calls. High values are usually bad.

This is average; check the MPI breakdown for advice on reducing it.

Time spent in filesystem I/O. High values are usually bad.

This is **negligible**; there's no need to investigate I/O performance.

This application run was CPU-bound. A breakdown of this time and advice for investigating further is in the CPU section below.

CPU

A breakdown of how the 56.5% total CPU time was spent:

Scalar numeric ops 27.7% Vector numeric ops 11.3% Memory accesses 60.9% Other 0.0

The per-core performance is memory-bound. Use a profiler to identify time-consuming loops and check their cache performance.

Little time is spent in vectorized instructions. Check the compiler's vectorization advice to see why key loops could not be vectorized.

MPI

Of the 43.5% total time spent in MPI calls:

Time in collective calls

Time in point-to-point calls

Estimated collective rate

169 Mb/s

Estimated point-to-point rate

50.6 Mb/s

The point-to-point transfer rate is low. This can be caused by inefficient message sizes, such as many small messages, or by imbalanced workloads causing processes to wait. Use an MPI profiler to identify the problematic calls and ranks.

I/O

A breakdown of how the 0.0% total I/O time was spent:

Time in reads 0.0%

Time in writes 0.0%

Estimated read rate 0 bytes/s

Estimated write rate 0 bytes/s

No time is spent in I/O operations. There's nothing to optimize here!

Memory

Per-process memory usage may also affect scaling:

Mean process memory usage 82.5 Mb

Peak process memory usage 89.3 Mb

Peak node memory usage 7.4%

The peak node memory usage is low. You may be able to reduce the total number of CPU hours used by running with fewer MPI processes and more data on each process.



PERFORMANCE REPORTS ACCERLERATOR

Accelerators

A breakdown of how accelerators were used:

GPU utilization	47.8%	
Global memory accesses	1.6%	
Mean GPU memory usage	0.8%	
Peak GPU memory usage	0.8%	

GPU utilization is low; identify CPU bottlenecks with a profiler and offload them to the accelerator.

The peak GPU memory usage is low. It may be more efficient to offload a larger portion of the dataset to each device.



NVIDIA VISUAL PROFILER

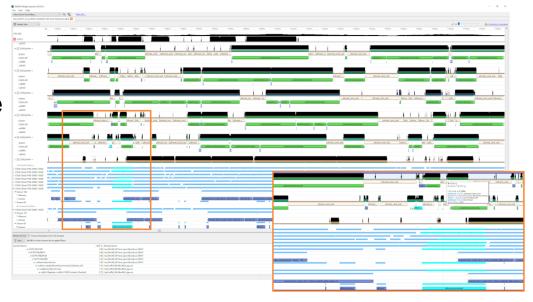
- Part of the CUDA Toolkit
- Supports all CUDA enabled GPUs
- Supports CUDA and OpenACC on Windows, OS X and Linux
- Unified CPU and GPU Timeline
- CUDA API trace
 - Memory transfers, kernel launches, and other API functions
- Automated performance analysis
 - Identify performance bottlenecks and get optimization suggestions
- Guided Application Analysis
- Power, thermal, and clock profiling



<u>NVIDIA VISUAL PROFILER: FXAMPLE</u> Timeline view 📰 Properties 🛭 📱 161.7 ms 161.8 ms 161.9 ms 162 ms rnel1DCT(float*, int, int, int) Process: 11119 Name Value ■ Thread: -1494415584 161.329 ms Start Runtime API 106.132 us Duration Driver API Grid Size [64,64,1] [0] GeForce GTX 480 ■ Context 1 (CUDA) Block Size [8,8,1] MemCpy (HtoD) 14 Registers/Thread MemCpy (DtoH) Memcpy DtoH [sync] Shared Memory/Block 512 bytes MemCpy (DtoD) Memory Compute CUDAkernelOua... CUDAkernel1IDCT(float*, int., Global Load Efficiency n/a 7 0.7% [101] CUD.. Global Store Efficiency 100% ₹ 0.3% [10] CUDAk... DRAM Utilization 10.9% CUDAkernelQua... T 0.0% [2] CUDAke. ▼ 0.0% [1] CUDAke.. CUDAkernel1IDCT(float*, int... Instruction T 0.0% [1] CUDAke.. Branch Divergence Overhe 0% ▼ 0.0% [1] CUDAke.. Total Replay Overhead 51% ▼ 0.0% [1] CUDAke... Shared Memory Replay Ov ▼ 0.0% [1] CUDAke.. Global Memory Replay Ove 6 Detailed Streams Global Cache Replay Overh Stream 1 CUDAkernelQua... CUDAkernel1IDCT(float*, int.. Memcpy DtoH [sync] Local Cache Replay Overhe information on Occupancy Kernel execution 📰 Analysis 🏻 📷 Details 💻 Console 📰 Settings Analysis Results Reset All Analyze All High Branch Divergence Overhead [35.1% avg, for kernels accounting for 1.9% of compute] **⊘ Timeline** Divergent branches are causing significant instruction issue overhead. More... High Instruction Replay Overhead [46.6% avg, for kernels accounting for 39.1% of compute] **⊘** Multiprocessor A combination of global, shared, and local memory replays are causing significant instruction issue overhead. More... **⊘** Kernel Memory High Global Memory Instruction Replay Overhead [45.9% avg, for kernels accounting for 39.1% of compute] Non-coalesced global memory accesses are causing significant instruction issue overhead. More... **Kernel Instruction** Automatic analysis of performance bottlenecks

NVIDIA NSIGHT SYSTEMS

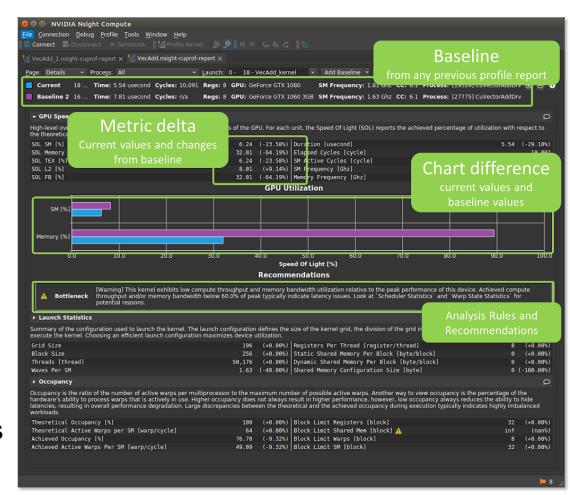
- System wide performance analysis tool
- High-level, low overhead
- Similar functionality as NVVP
 - No automated/guided analysis
 - Can launch Nsight Compute for in-depth kernel analysis
- CLI and GUI





NVIDIA NSIGHT COMPUTE

- Interactive kernel profiler
- Detailed performance metrics
- Guided analysis
- Baseline feature to compare versions
- Customizable and data-driven UI
- Supports analysis scripts for post-processing results
- CLI and GUI





SCORE-P

 Community instrumentation and measurement infrastructure



 Developed by a consortium of performance tool groups









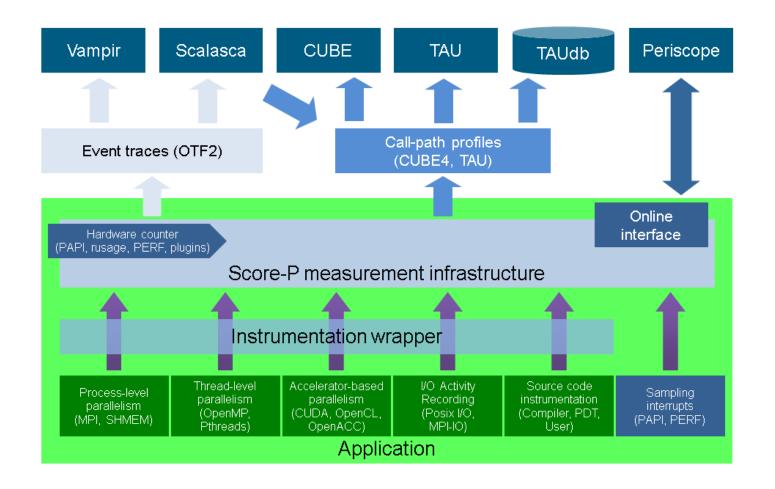




- Next generation measurement system of
 - Scalasca 2.x
 - Vampir
 - TAU
 - Periscope
- Common data formats improve tool interoperability
- http://www.score-p.org



SCORE-P OVERVIEW





SCORE-P GPU MEASUREMENTS

OpenACC

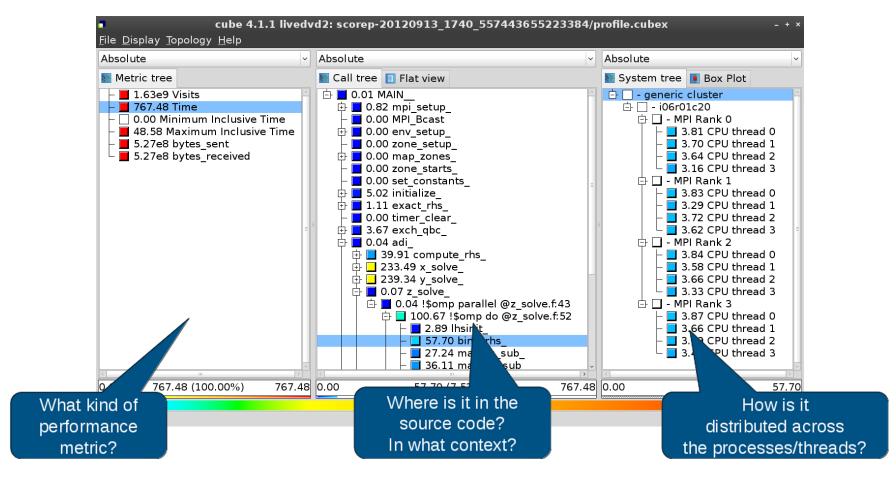
- Prefix compiler and linker command with scorep --openacc
- export ACC_PROFLIB=\$SCOREP_ROOT/lib/libscorep_adapter_openacc_event.so
- export SCOREP_OPENACC_ENABLE=yes
- yes refers to: regions, wait, enqueue
- Full list of options in User Guide

CUDA

- Prefix compiler and linker command with scorep --cuda
- export SCOREP_CUDA_ENABLE=yes
- yes refers to: runtime, kernel, memcpy
- Full list of options in User Guide
- OpenCL similar (use SCOREP_OPENCL_ENABLE=yes)

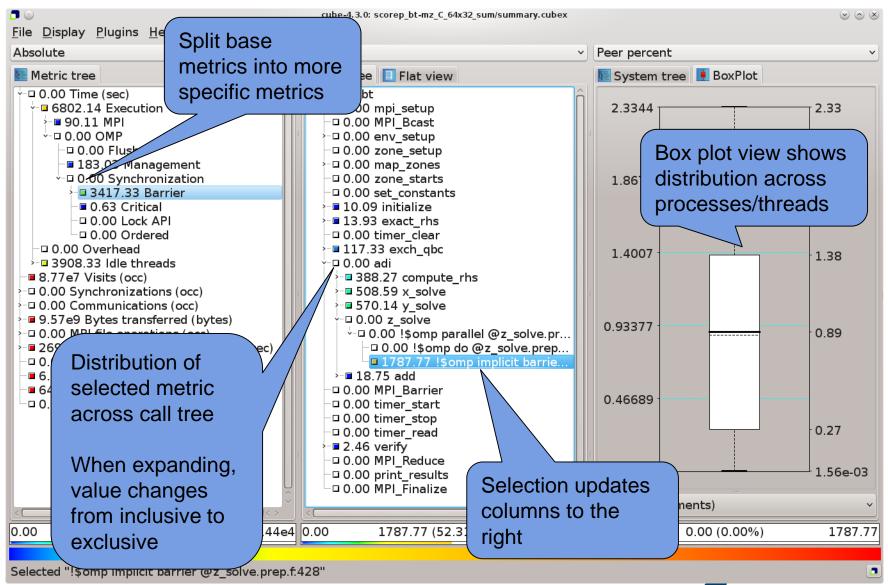


CUBE OVERVIEW

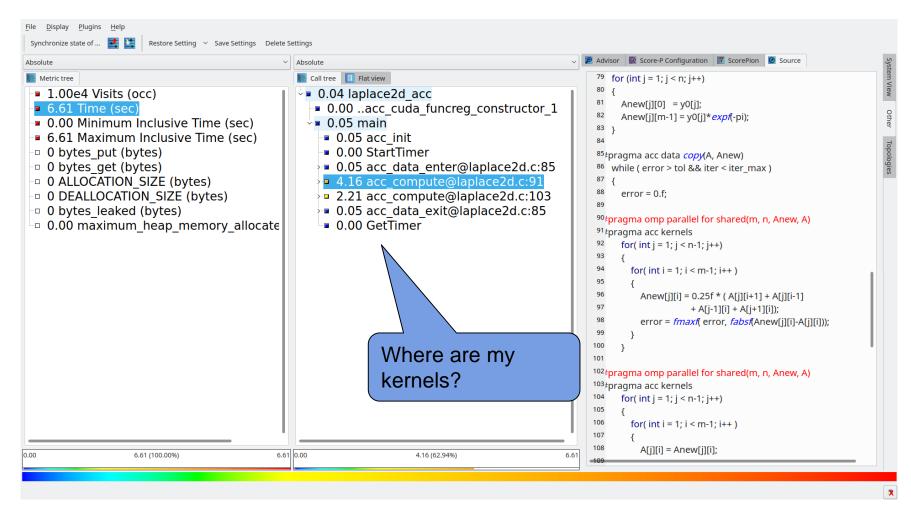




CUBE – OVERVIEW



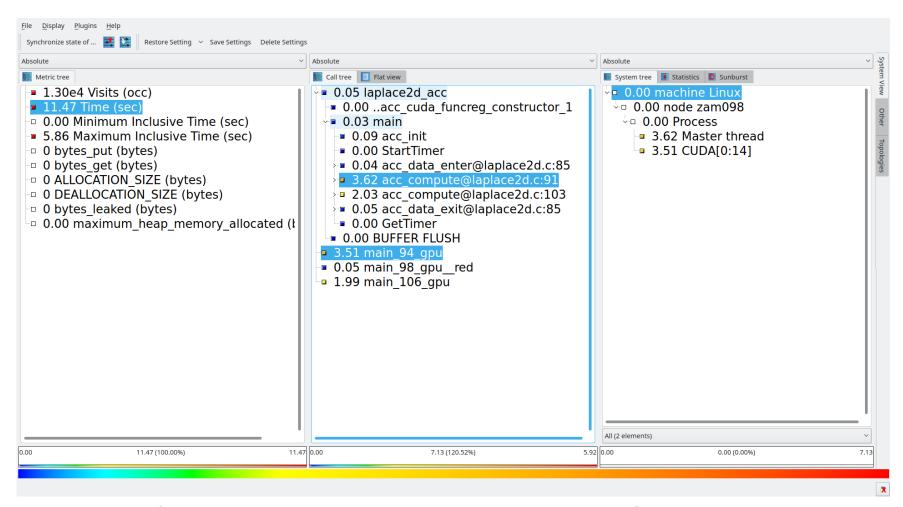
EXAMPLE: OPENACC



Pure OpenACC measurements give host-side events only



EXAMPLE: OPENACC + CUDA



Enabling CUDA also shows kernels on the GPU



VAMPIR EVENT TRACE VISUALIZER

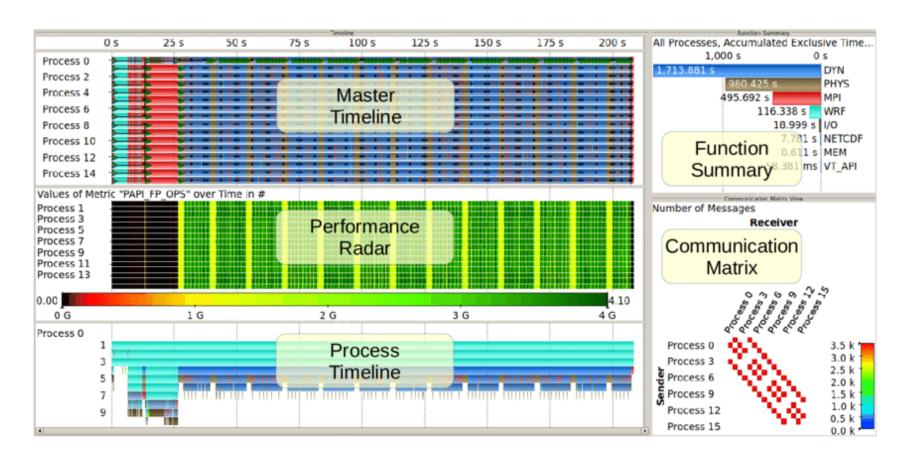
 Offline trace visualization for Score-P's OTF2 trace files



- Visualization of MPI, OpenMP, GPU and application events:
 - All diagrams highly customizable (through context menus)
 - Large variety of displays for ANY part of the trace
- http://www.vampir.eu
- Advantage:
 - Detailed view of dynamic application behavior
- Disadvantage:
 - Requires event traces (huge amount of data)
 - Completely manual analysis

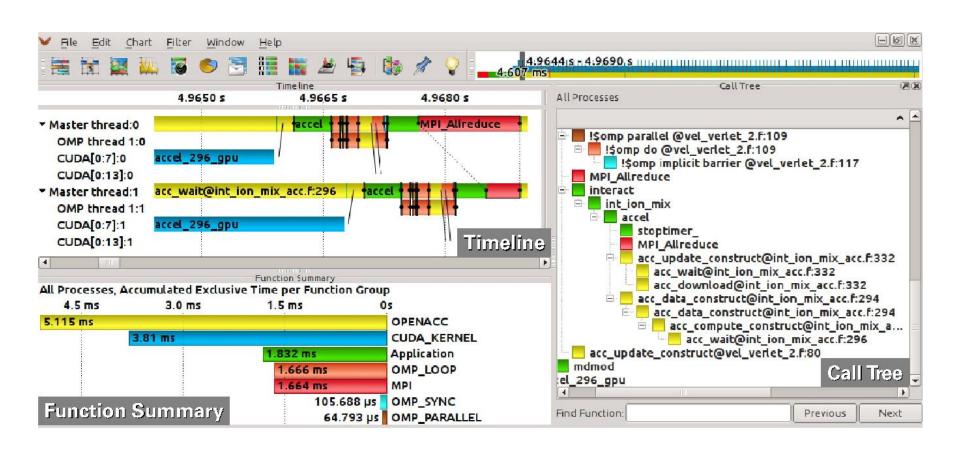


VAMPIR DISPLAYS





VAMPIR COMPLEX APPLICATION





REMARK: NO SINGLE SOLUTION IS SUFFICIENT!



- A combination of different methods, tools and techniques is typically needed!
 - Analysis
 - Statistics, visualization, automatic analysis, data mining, ...
 - Measurement
 - Sampling / instrumentation, profiling / tracing, ...
 - Instrumentation
 - Source code / binary, manual / automatic, ...



WHAT NOW?

- The tools are there what now?
- Development phase:
 - Use NVDIA tools
 - Debug: CUDA-MEMCHECK/CUDA-GDB
 - Performance: Nsight Systems and Compute
- Scaling up:
 - Use 3rd-party tools
 - Debug: TotalView/DDT
 - Performance: Score-P, Vampir

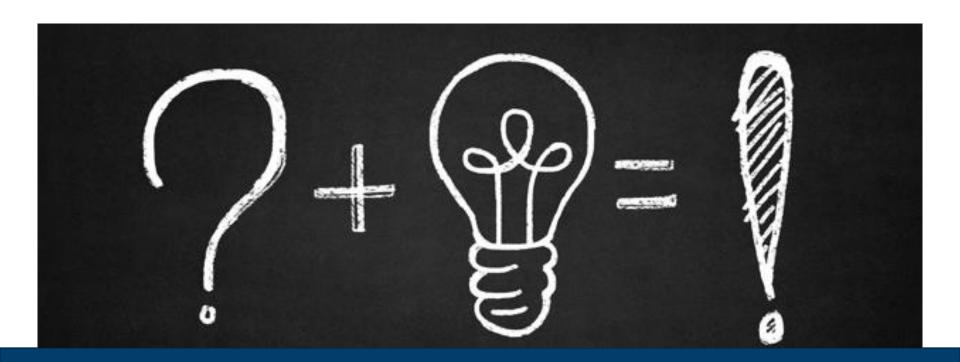


NEED HELP?

- Talk to the experts
 - NVIDIA Application Lab
 - JSC team "Performance Analysis"
 - JSC team "Application Optimization"
 - Apply for a POP audit

Successful performance engineering often is a collaborative effort





QUESTIONS

